

Industry Institution Interaction Cell (IIIC)

Date: 27-03-2026

REPORT
on
INDUSTRY EXPERT TALK
**“SEMICONDUCTOR ENGINEERING:
A PERSPECTIVE & OPPORTUNITIES”**

Program Particulars:

Date Organized	Title of Program	Name of Resource Persons	Participants
26 th March 2026 (Thursday)	“Semiconductor Engineering : A Perspective & Opportunities”	1. Dr. C.Srinivas S Sarma, Founder & Director 2. Ms. K.Renuka, Manager 3. Ms.J.Madhurya Deepti, Engineer 4. Mr.C.Pavan Kalyan, Manager WnP Semicon, Hyderabad	B. Tech. ECE,CSE,AIIML,DS Students (90 Students) and faculty members

 **NALLA NARASIMHA REDDY**
Education Society's Group of Institutions—Integrated Campus
UGC AUTONOMOUS INSTITUTION
Industry Institute Interaction Cell (IIIC)
Organizing
Industry Expert Talk
on
"Semiconductor Engineering - A Perspective & Opportunities"

Resource Person's



Dr. C Srinivasa S Sarma
Founder & Director



Ms. K. Renuka
Manager, Physical Design





Ms. J Madhurya Deepti
Digital Verification Engineer



Mr. C Pavan Kalyan
Manager, Product Design

Date: 26th March 2026 **Venue: Conference Hall, Ground Floor** **Time: 11:00 AM**

Program Flexi

Overview of Program:

The Industry Institute Interaction Cell (IIIC) of Nalla Narasimha Reddy Education Society's Group of Institutions (NNRG), Hyderabad, organized an Industry Expert Talk on "Semiconductor Engineering A Perspective & Opportunities" on 26th March 2026 at 11:00 AM in the Conference Hall, Ground Floor, NNRG Campus. The session was delivered by Dr.C.Srinivas S Sarma, Founder & Director , WnP Semicon, Hyderabad.

Purpose of Program:

The purpose of the program was to provide students with a clear perspective on semiconductor engineering by introducing them to the current industry landscape, emerging technologies, and future trends, while also highlighting the wide range of career opportunities available in the semiconductor sector. It aimed to bridge the gap between academic knowledge and industry expectations by creating awareness about essential skills and real-world applications, and to motivate students to explore and prepare for careers in the rapidly growing semiconductor ecosystem, particularly in the Indian context.



Start of Program, anchoring was done by Ms.Srija-Final Year ECE Student



Dr.G.Janardhana Raju, Dean, SoE welcoming the resource persons



Founder & Director briefing about company profile

Program Highlights:

The program featured four distinguished resource persons, each delivering expert sessions on different topics in semiconductor engineering.

The first resource person presented on **IC Design Strategies**, explaining the various stages involved in integrated circuit design, including system-level specification, architecture design, modeling, verification, and fabrication processes, giving students a complete overview of the IC design flow.



Dr.C.Srinivasa sir explaining the company's requirements and vital concepts

The second resource person delivered a session on **Circuit-Level Analog & RF Design**, where key concepts such as analog circuit design, RF systems, DAC circuits, and low-noise amplifier (LNA) design were discussed. The session provided insights into real-time circuit implementations and practical design considerations.



(L) Mr.Pavan sir briefing about physical designs



(R)Ms Deepti mam pointing the need of VLSI

The third resource person covered **Digital Synthesis**, focusing on how high-level hardware description language (HDL) designs are translated into gate-level implementations. The session emphasized synthesis processes such as translation, optimization, and mapping, helping students understand how digital designs are made and physically realized.

The fourth resource person presented on **Physical Design Placement**, explaining stages like floor planning, global placement, detailed placement, optimization, and verification. This session helped students understand how chip layout is organized and optimized for performance, power, and area in real-world semiconductor design.



Ms.K.Renuka madam highlighting the digital layout designs



Participants in the program session



Post program Discussion with the WnP Company



Team

Vote of thanks:

The program concluded with a heartfelt Vote of Thanks delivered by Dr. B. Hariprasad Naik, Professor, Dept of ECE. He expressed sincere gratitude to all the resource persons for sharing their valuable knowledge and insights on semiconductor engineering. He also thanked the management, faculty members, and organizing team for their efforts in successfully conducting the program. Special appreciation was extended to the students for their active participation and enthusiastic involvement, which contributed to the overall success of the event.



Dr.H.P.Naik sir expressing his views followed by vote of thanks

Outcome of Program:

A total of 90 students attended the program and actively participated by raising queries, seeking suggestions, and engaging in discussions to gain deeper insights into the semiconductor domain. Students gained clarity on semiconductor career opportunities, industry expectations, and the necessary skill development paths. The program also strengthened industry–institute interaction by providing direct exposure to expert knowledge and real-time applications. Overall, the sessions were informative and impactful, enhancing student awareness and motivating them to pursue careers in semiconductor engineering.

Dean, SoE & Convener, IIC