

SCHOOL OF ENGINEERING
Department of Electronics and Communication Engineering

A Report on Engineers Day

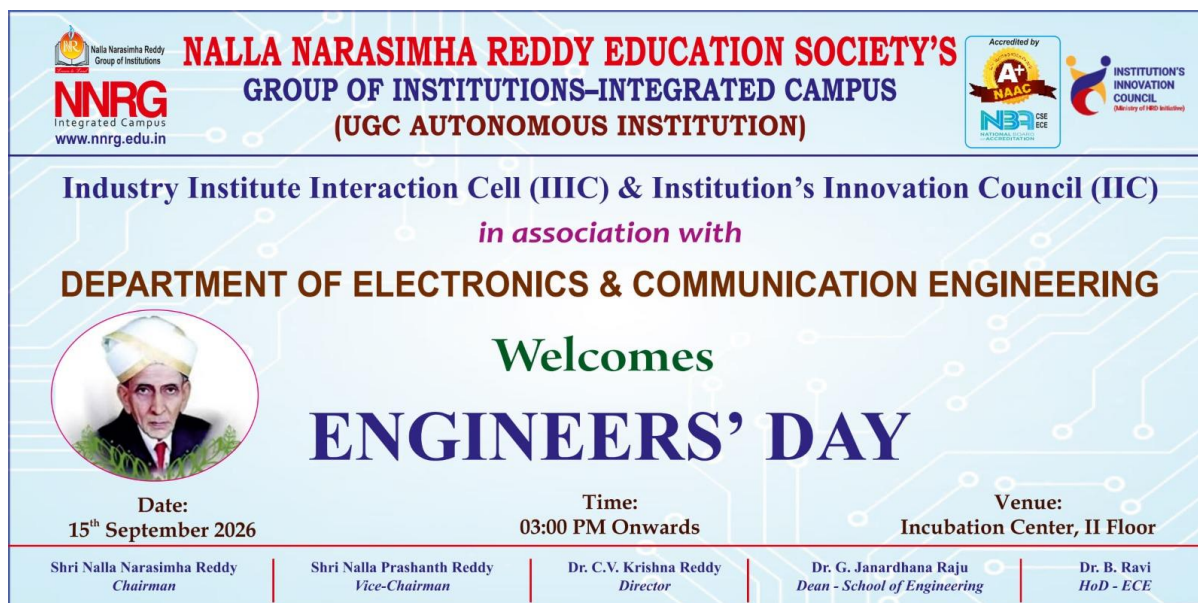
DATE: 15/9/2026

VENUE: ECE Seminar Hall, Second Floor

NO OF PARTICIPANTS: 40

TIME: 03:00 PM

PARTICIPANTS TYPE: Students of ECE



The banner features a blue background with a circuit pattern. At the top, it includes the NNRG logo and accreditation details. The main text reads: 'NALLA NARASIMHA REDDY EDUCATION SOCIETY'S GROUP OF INSTITUTIONS-INTEGRATED CAMPUS (UGC AUTONOMOUS INSTITUTION)'. Below this, it states 'Industry Institute Interaction Cell (IIIC) & Institution's Innovation Council (IIC) in association with DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING Welcomes ENGINEERS' DAY'. A portrait of a man in a white turban is on the left. The event details are: Date: 15th September 2026, Time: 03:00 PM Onwards, Venue: Incubation Center, II Floor. At the bottom, a table lists the organizing committee members.

Shri Nalla Narasimha Reddy Chairman	Shri Nalla Prashanth Reddy Vice-Chairman	Dr. C.V. Krishna Reddy Director	Dr. G. Janardhana Raju Dean - School of Engineering	Dr. B. Ravi HoD - ECE
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Introduction

To commemorate the birth anniversary of Bharat Ratna Sir M. Visvesvaraya, observed nationwide as Engineer's Day, the Department of Electronics and Communication Engineering (ECE) at Nalla Narasimha Reddy Education Society's Group of Institutions, in association with the Industry Institute Interaction Cell (IIIC) and Institution's Innovation Council (IIC), conducted a competitive Technical Quiz Program on 15th September 2026. The program was designed to stimulate technical curiosity, reinforce fundamental engineering concepts, enhance analytical agility, and align co-curricular activities directly with Outcome-Based Education (OBE).

Objectives

- To commemorate Engineer's Day and instil ethical, societal, and professional engineering values inspired by Sir M. Visvesvaraya.
- To assess and consolidate core technical competencies in analog/digital electronics, communication systems, IoT, and embedded systems.
- To promote analytical reasoning, problem diagnosis, and rapid decision-making in a competitive environment.

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- To encourage collaborative teamwork, effective peer communication, and lifelong self-directed learning habits.

Learning Outcomes (LOs)

- LO1 (Engineering Knowledge):** Recall, apply, and demonstrate foundational principles of electronics, communication engineering, and contemporary technological advancements.
- LO2 (Problem Analysis & Critical Thinking):** Interpret complex technical questions, diagnose system-level issues rapidly, and select appropriate analytical solutions under time constraints.
- LO3 (Teamwork & Communication):** Engage effectively in peer discussions, demonstrate collaborative consensus-building, and articulate technical answers clearly within team rounds.

Event Schedule & Execution

3:00 PM – 3:15 PM: Welcome Address and Floral Tribute to Sir M. Visvesvaraya.

3:15 PM – 3:30 PM: Well-structured technical quiz round comprising 30 multiple-choice questions spanning Electronic Devices & Circuits, Signals & Systems, Digital Electronics, and Core engineering.



3:30 PM – 4:00 PM: Announcement of Results, Feedback Collection, and Certificate Distribution.

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Winners

Prize	Year	Roll Number	Name
I	III -ECE	247Z1A0490	K. Surya
		247Z1A0477	K. Harsha
II	III -ECE	247Z1A04E5	P. Nandini
		247Z1A04E6	R. Sai Priya
III	III -ECE	247Z1A04B9	M. Niharika
		247Z1A04B3	M. Jeshwanth
		247Z1A04H2	T. Sravanthi
		247Z1A04E1	P. Soujanya


Coordinator


HoD