

Nalla Narasimha Reddy Group of Institutions

Report On

Guest Lecture on “Future Prospects in field of ECE”

by

Mr. K.Raghava

Under IETE, ISF-NNRG Professional chapter

The guest lecture was conducted by the department of E.C.E on 11th July, 2014. It was organized under the professional chapter of IETE.

He initiated the lecture by giving basic introduction about VLSI and its applications. He mentioned about the importance of VLSI Circuit design and he emphasized that, two parameters are being considered one is system area and another one is power consumption of circuits. He incorporated some ideas about the various VLSI tools used in the front end and back end design. He gave an immense idea about 3D technology process. He also gave brief explanation about the following content.

- How the Integrated circuit future performance improvements will come through circuit, system architecture, and software advancements
- Initial 3D technology demonstrations (at MIT-LL) are centered around advanced focal plane architectures
- Full impact of 3D integration.
- Potential application areas include: High-end focal planes, FPGAs, Dense memory, memory on process



With
Respectfully,
K. Raghava
Head of the Department
Electronics & Communication Engineering
Nalla Narasimha Reddy Group of Institutions
Central Institute of Technology & Research
Nalla Narasimha Reddy Group of Institutions